

| | Type | Hits | Search Text |
|-----------|------|------|--|
| 1 | BRS | 2 | fractional-division and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 2 | BRS | 59 | (modem or (modulator or demodulator)) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 3 | BRS | 1 | (master-clock) and (modem or (modulator or demodulator)) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 4 | BRS | 4 | (master near clock) and (modem or (modulator or demodulator)) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 5 | BRS | 1 | (base adj signal) and (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 6 | BRS | 6 | (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 7 | BRS | 25 | (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "370"/\$.ccls. |
| 8 | BRS | 36 | (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 9 | BRS | 3 | ((master near clock) or master-clock) and power and (hietala near alexander).in. |
| 10 | BRS | 23 | (acurar\$4 or synchronous) and (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 11 | BRS | 54 | (acura\$4) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "455"/\$.ccls. |
| 12 | BRS | 32 | (acura\$4) and (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 13 | BRS | 34 | (acura\$4 or asynchronous) and (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 14 | BRS | 22 | ((high near accura\$4) or asynchronous) and (master near clock) and (phase-locked adj loop) and (clock adj signal) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 15 | BRS | 39 | (phase near locked) and 455/343.1,343.2.ccls. |
| 16 | BRS | 11 | accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 17 | BRS | 6 | synthesizer and accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 18 | BRS | 2 | master and synthesizer and accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 19 | BRS | 3 | base and synthes\$4 and accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 20 | BRS | 5 | (frequency near synthesizer) and accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 21 | BRS | 3 | fraction\$4 and (frequency near synthesizer) and accuracy and (phase near locked) and 455/343.1,343.2.ccls. |
| 22 | BRS | 14 | (frequency near synthesizer) and accuracy and (fraction\$3I near division) and (phase near locked) and "455"/\$.ccls. |
| 23 | BRS | 16 | (frequency near synthesizer) and accuracy and (fraction\$3 near division) and (phase near locked) and "455"/\$.ccls. |
| 24 | BRS | 19 | (frequency near synthesizer) and accuracy and (fraction\$3 near division) and "455"/\$.ccls. |
| 25 | BRS | 45 | (frequency near synthesizer) and (fraction\$3 near division) and "375"/\$.ccls. |
| 26 | BRS | 2 | (sleep near mode) and (asano near nobuo).in. |

| | Type | Hits | Search Text |
|-----------|------|------|---|
| 27 | BRS | 1 | (frequency near divider) and (asano near nobuo).in. |
| 28 | BRS | 12 | (frequency) and (asano near nobuo).in. |
| 29 | BRS | 27 | (power) and (asano near nobuo).in. |
| 30 | BRS | 1 | (frequency near generator) and "5987339".uref. |
| 31 | BRS | 3 | (frequency near oscillator) and "6029061".uref. |
| 32 | BRS | 1 | (phase-locked adj loop) and (frequency near algorithm) and (frequency adj synthesiz\$3) and (power) and "375"/\$.ccls. |
| 33 | BRS | 10 | (frequency near algorithm) and (frequency adj synthesiz\$3) and "375"/\$.ccls. |
| 34 | BRS | 27 | (frequency near algorithm) and synthesiz\$3 and "375"/\$.ccls. |